

DOCKET NO. SC12866TP

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Law Department
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MD: TX32/PL02
Austin, TX 78729Telephone: (512) 996-6839
Facsimile: (512) 996-6854**RECEIVED**
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Date: August 2, 2004

To: Edward Joseph Wojciechowicz - 2815

Location: United States Patent and Trademark Office

Fax No.: (703) 872-9306

From: Joanna G. Chiu - 43,629

Subject: 10/662,832- Michael J. Rendon et al.

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MESSAGE:

Enclosed herewith, please find a **FORMAL AMENDMENT** for filing in the below-identified application.

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1. <input checked="" type="checkbox"/>	1 page Facsimile Cover Sheet
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PLEASE GIVE THESE PAPERS TO:

EXAMINER: Edward Joseph Wojciechowicz
GROUP ART UNIT: 2818
SERIAL NO.: 10/662,832
FILED: SEPTEMBER 15, 2003
INVENTOR: MICHAEL J. RENDON ET AL.

DOCKET NO. SC12866TP

UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICANT(S) Michael J. Rendon et al.

GROUP ART UNIT: 2815

AUG 02 2004

APPLN. NO.: 10/662,832

EXAMINER: Edward Joseph
Wojciechowicz

FILED: September 15, 2003

TITLE: SEMICONDUCTOR DEVICE HAVING AN INSULATING LAYER
AND METHOD FOR FORMING

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated July 1, 2004, and Examiner's comments with regard thereto, please enter the following amendments in the above-entitled application, without prejudice or disclaimer.

REMARKS

Claims 1-34 are subject to a restriction requirement: Group I (claims 32-34) and Group II (claims 1-31). Applicants elect to prosecute the claims of Group II (claims 1-31). Applicants respectfully solicit allowance of the pending claims. Contact me if there are any issues regarding this communication or the current Application.

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc.

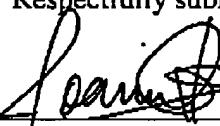
Respectfully submitted,

SEND CORRESPONDENCE TO:

Freescale Semiconductor, Inc.
Law Department

Customer Number: 23125

By:



Joanna G. Chiu
Attorney of Record
Reg. No.: 43,629
Telephone: (512) 996-6839
Fax No.: (512) 996-6854